Taiwan Semiconductor Manufacturing Company, Ltd. (the “TSMC”) has made following efforts to ensure the semiconductor manufacturing process in accordance with the Part A and B of IEEE Standard for Environmental and Social Responsibility Assessment of Computers and Displays (IEEE Std 1680.1TM-2018) Criterion 4.1.10.2 Optional - Reduce fluorinated greenhouse gas emissions from semiconductor production. The fluorinated greenhouse gas (F-GHG) emissions inventory has been reported and F-GHG emissions have been reduced for all TSMC’s 300mm process semiconductor manufacturing facilities, which including Fab 12A, Fab 12B, Fab 14A, Fab 14B, Fab 15A, Fab 15B, Fab 18 and Advanced Backend Fab3 (the “Fabs in scope”).

For Part A
TSMC develops a F-GHG emissions inventory by the method of the IPCC 2006 Tier 2b methodology, multiply the IPCC 2006 Tier 2b emissions by a factor of 1.13 to obtain adjusted IPCC 2006 Tier 2b emissions from cleaning processes with 100-year global warming potentials (GWPs) from the IPCC Fourth Assessment (IPCC AR4). In addition, TSMC annually commissions a third-party to perform a verification of greenhouse gas assertions, including emissions of fluorinated GHGs used in plasma etching/wafer cleaning, chamber cleaning processes and heat transfer fluid use, on the basis of ISO 14064-1: 2016, and publicly reports the result on our CSR report.

For Part B
Our abatement equipment is electrically heated, fuelled-combustion, plasma, and catalytic devices that are specifically designed to abate F-GHGs, are used within the manufacturer’s specified process window and in accordance with specified maintenance schedules, and whose DREs have been measured and confirmed under actual process conditions, using a technically sound protocol, which accounts for known measurement errors including, for example, CF₄ by-product formation during C₂F₆ abatement as well as the effect of dilution, the use of oxygen or both in combustion abatement systems.

We calculate that F-GHG emissions based on the equation stated below and the reduction percentage we are adopted is >75% as the fluorinated heat transfer fluids (F-HTFs) are excluded from the reduction assessment.

Percent of Total Reduction Emissions = \[ \frac{100\% \times \left[ 1 - \frac{\sum T E_{FAB}^{\text{TE}}} {\sum B E_{FAB}^{\text{BE}}} \right]} \]

where:
- \( T E_{FAB} \) is the total emissions per fab in scope calculated using the methodology in Part A.
- \( B E_{FAB} \) is the baseline emissions per fab calculated using the following equation.
BE\textsubscript{FAB} equation per fab:

\[
BE_{FAB} = 1.15 \times [(CEW \times WF_{EW}) + (CCC \times WF_{CC})]
\]

where:

- 1.15 is the factor to account for fluorinated heat transfer fluid emissions (omit if excluding HTFs)
- \(CEW\) is the total consumption of all F-GHGs by all etching and wafer cleaning processes within the fab in reporting year.
- \(WF_{EW}\) is the weighting factor for etching and wafer cleaning that is 5940.
- \(CCC\) is the total consumption of all F-GHGs by all CVD chamber cleaning processes within the fab in reporting year.
- \(WF_{CC}\) is the weighting factor for CVD chamber cleaning that is 8260.

F-GHG Reduction Emissions Result for Y2019 to the Fabs in Scope

<table>
<thead>
<tr>
<th>Fabs in Scope</th>
<th>Fab 12A</th>
<th>Fab 12B</th>
<th>Fab 14A</th>
<th>Fab 14B</th>
<th>Fab 15A</th>
<th>Fab 15B</th>
<th>Fab 18</th>
<th>Advanced Backend Fab 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Percentage reduced = (1 - (TE\textsubscript{FAB}/BE\textsubscript{FAB}))</td>
<td>91.9%</td>
<td>97.1%</td>
<td>93.8%</td>
<td>95.9%</td>
<td>96.7%</td>
<td>95.5%</td>
<td>96.7%</td>
<td>90.6%</td>
</tr>
<tr>
<td>(TE\textsubscript{FAB}) (MT CO\textsubscript{2}e)</td>
<td>101,366</td>
<td>16,843</td>
<td>139,950</td>
<td>96,438</td>
<td>77,762</td>
<td>131,845</td>
<td>6,382</td>
<td>2,718</td>
</tr>
<tr>
<td>(BE\textsubscript{FAB}) (MT CO\textsubscript{2}e)</td>
<td>1,255,337</td>
<td>579,810</td>
<td>2,241,763</td>
<td>2,380,852</td>
<td>2,383,877</td>
<td>2,956,904</td>
<td>192,870</td>
<td>29,054</td>
</tr>
<tr>
<td>(CEW) (kg)</td>
<td>43,878</td>
<td>23,854</td>
<td>77,665</td>
<td>78,664</td>
<td>65,304</td>
<td>174,906</td>
<td>3,733</td>
<td>4,748</td>
</tr>
<tr>
<td>(CCC) (kg)</td>
<td>120,424</td>
<td>53,041</td>
<td>215,549</td>
<td>231,669</td>
<td>241,643</td>
<td>232,199</td>
<td>20,666</td>
<td>103</td>
</tr>
</tbody>
</table>

Taiwan Semiconductor Manufacturing Company

Han-Wen Fung  
Director of Corporate ESH Division  
Date: May 25th, 2020